

FIG. 1

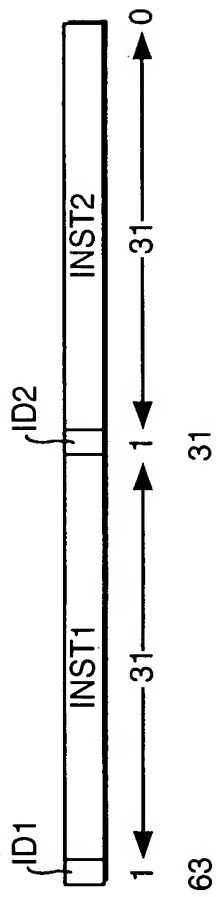


FIG. 2a

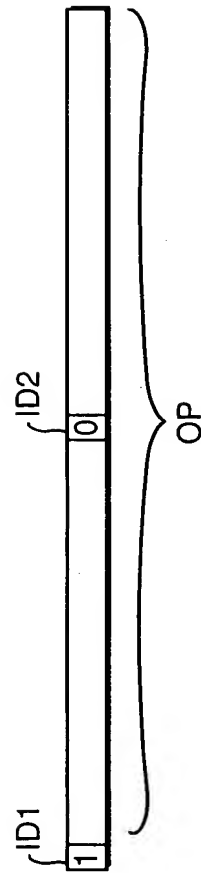


FIG. 2b

IDbit

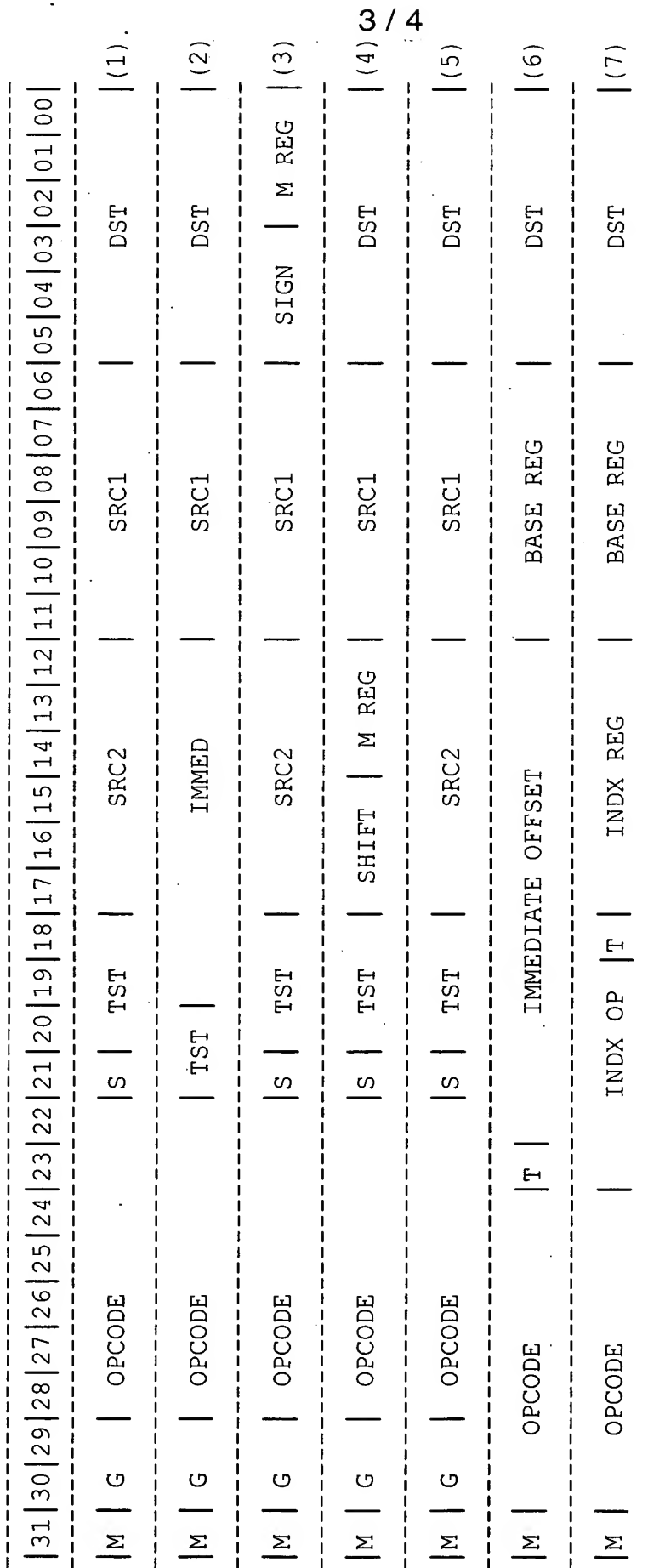


FIG. 3

BIT	31(M)	30(G)	29(G)
REGISTER/IMMEDIATE	1	0	X
REGISTER/REGISTER	1	1	0
MAC OPERATIONS	1	1	1

FIG. 4

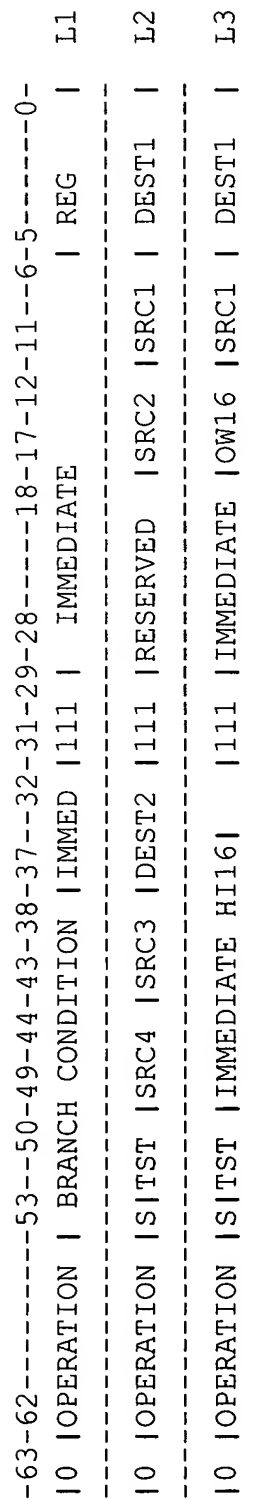


FIG. 5